

FIG. 1

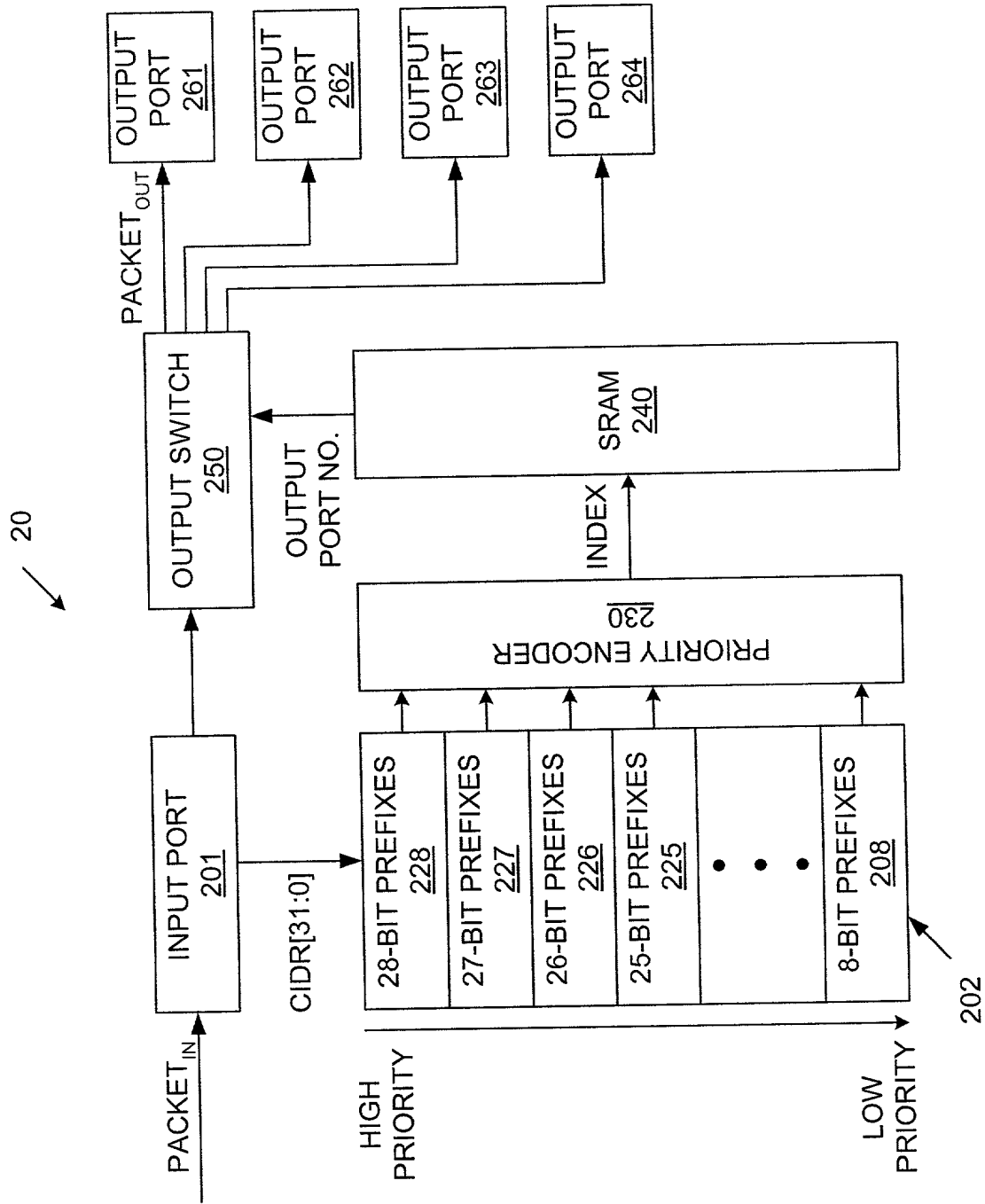


FIG. 2  
(PRIOR ART)

FIG. 3 is a block diagram of a CAM array 30.

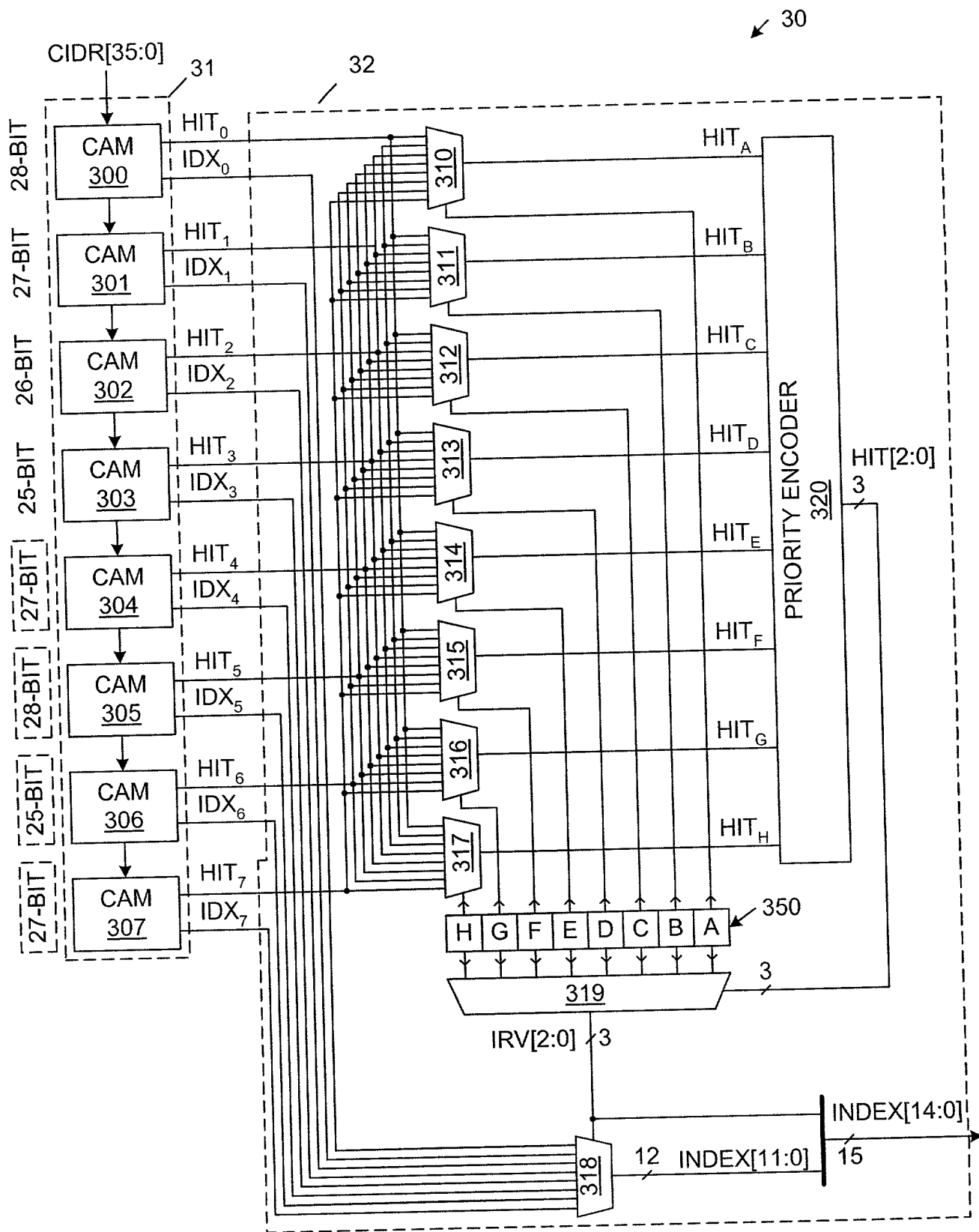


FIG. 3

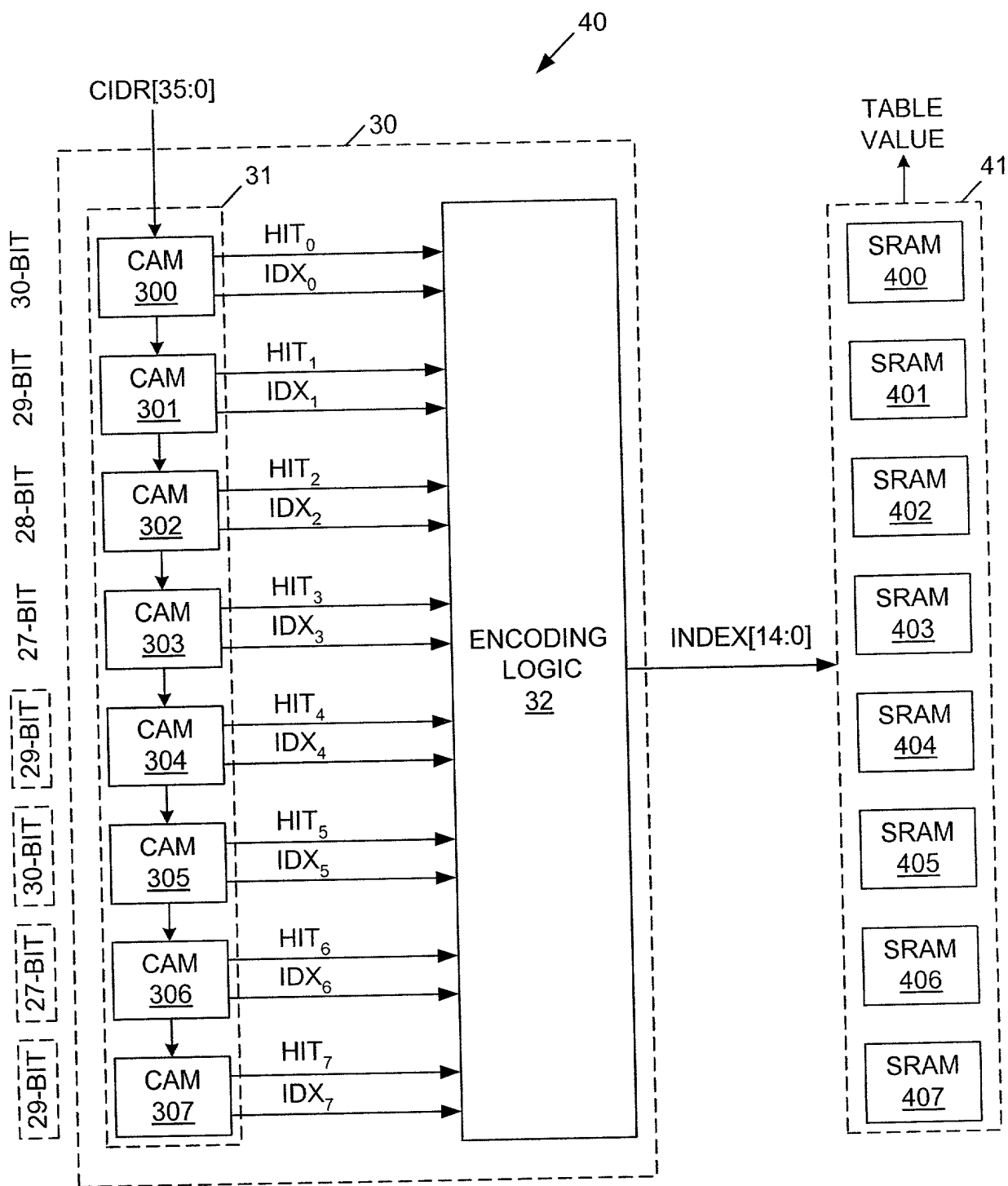


FIG. 4

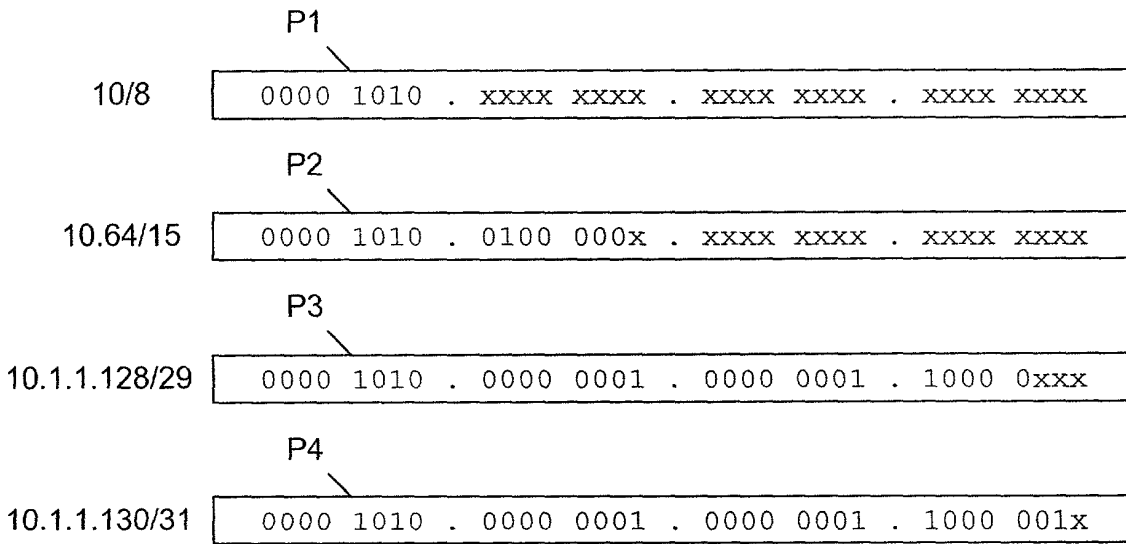


FIG. 5

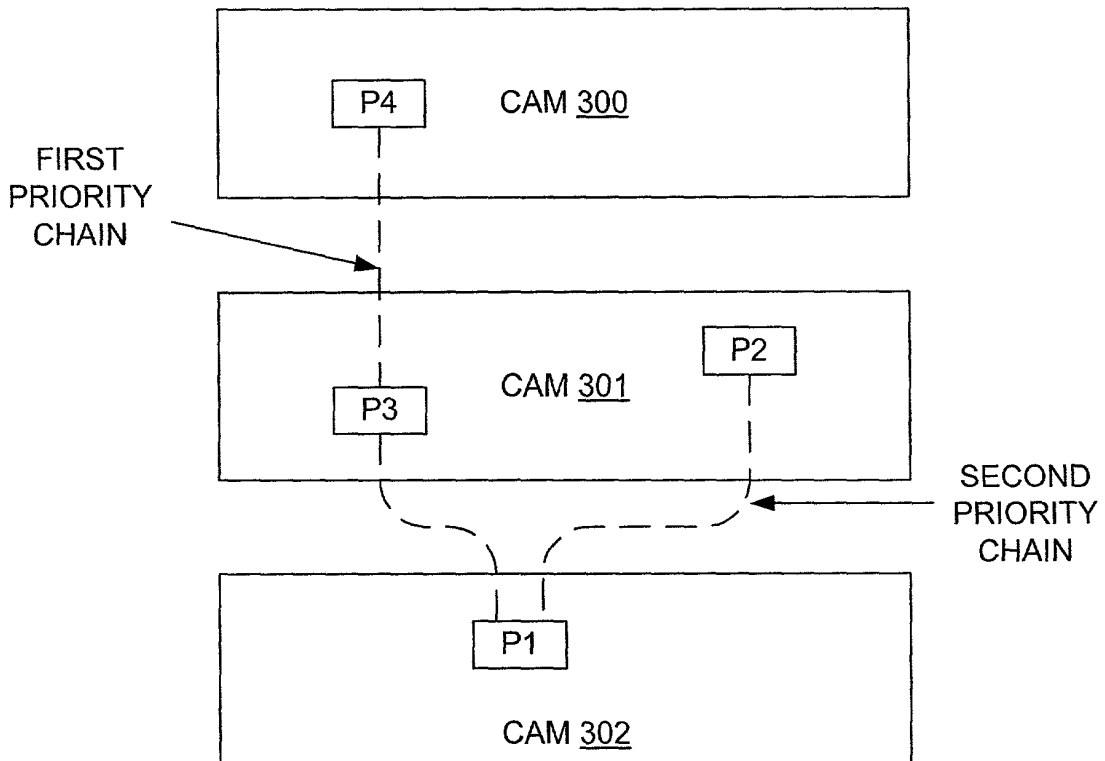


FIG. 6